INVENTORS NAME: Dean Warren et al. DOCKET NO.: 884.311US1

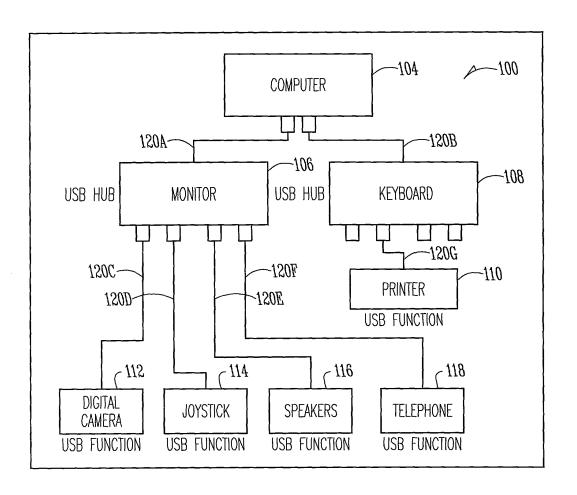


Fig. 1

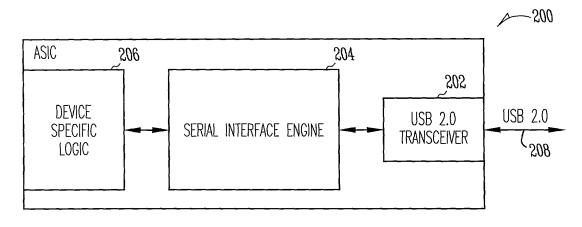


Fig.2

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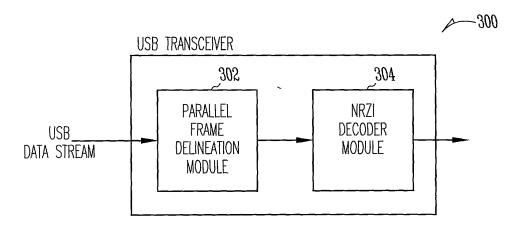
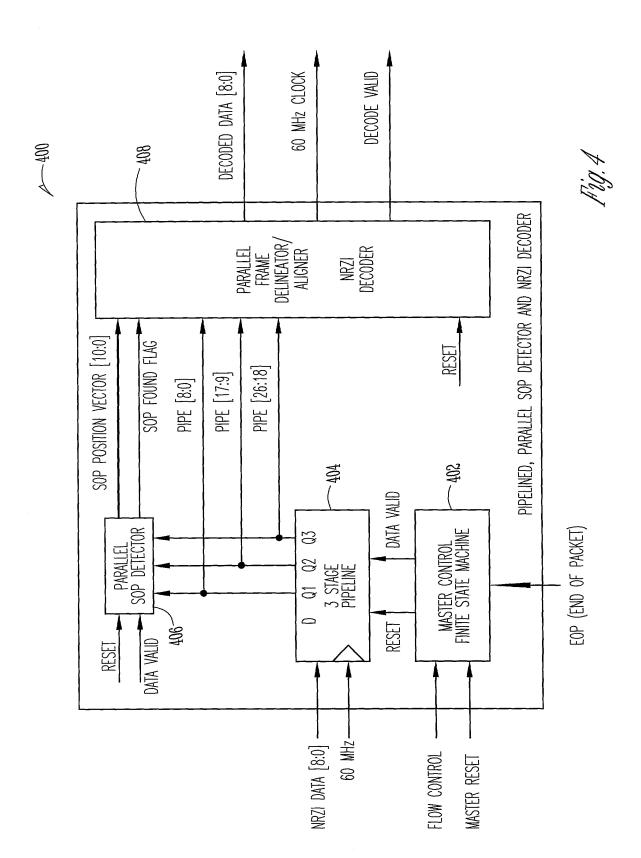


Fig.3

DECODER
INVENTORS NAME: Dean Warren et al.
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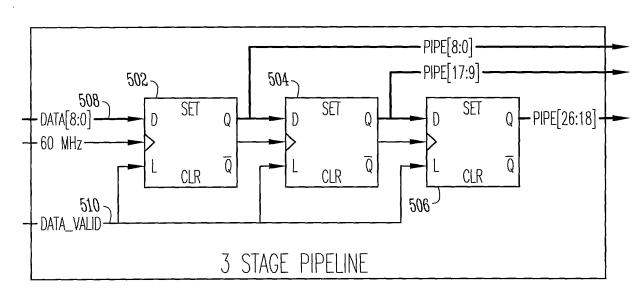


Fig. 5

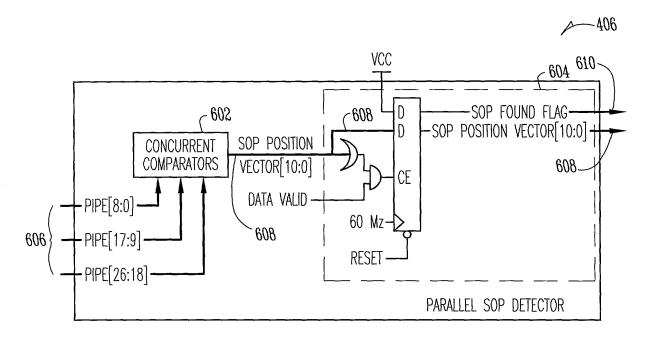
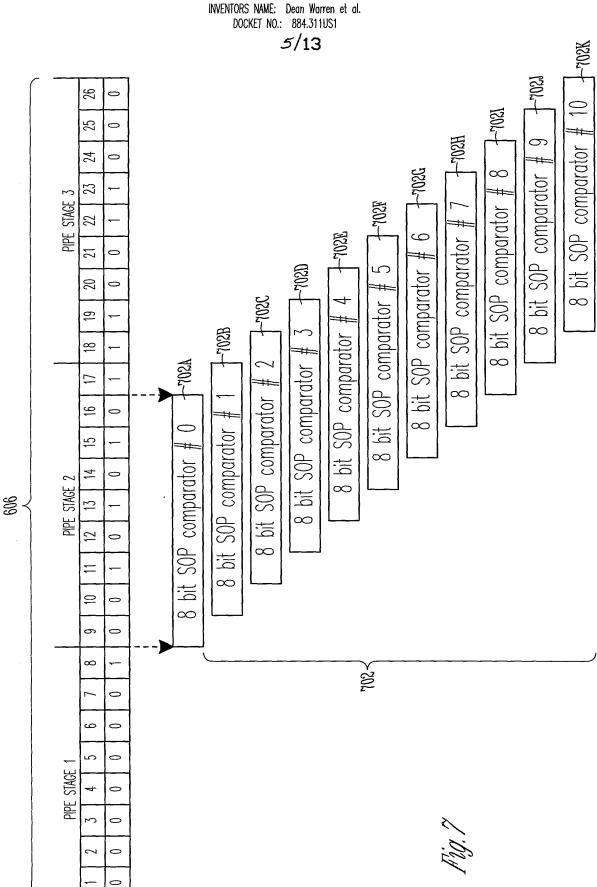


Fig. 6

DECODER



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Position vector[10:0]

												_
	10	9	8	7	6	5	4	3	2	1	0	
		0	^	^	0	0	0	n		0	1	
	U	U	U	0	U	U 	U	U	U	U	<u> </u>	800
									800	800B		AO

Fig. 8

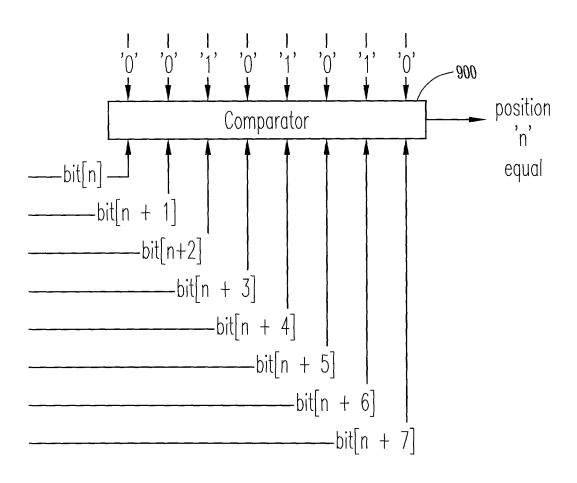


Fig. G

INVENTORS NAME: Dean Warren et al. DOCKET NO.: 884.311US1

$$-bit[n]$$

$$-bit[n + 1]$$

$$-bit[n + 2]$$

$$-bit[n + 3]$$

$$-bit[n + 4]$$

$$-bit[n + 5]$$

$$-bit[n + 6]$$

$$-bit[n + 7]$$

$$-bit[n + 7]$$

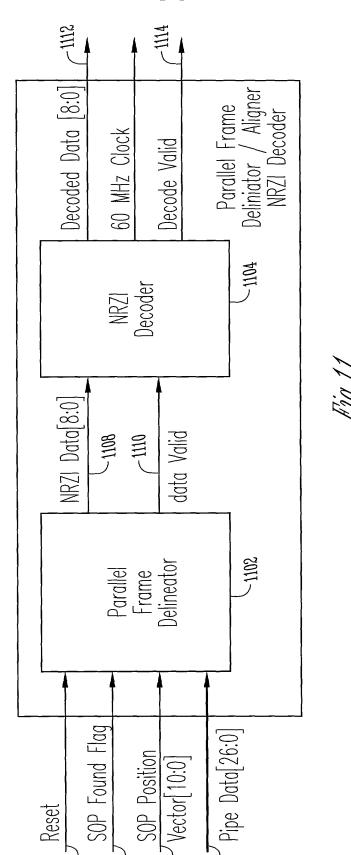
$$-bit[n + 7]$$

$$-bit[n + 7]$$

Fig. 10

INVENTORS NAME: Dean Warren et al. DOCKET NO.: 884.311US1

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INVENTORS NAME: Dean Warren et al. DOCKET NO.: 884.311US1

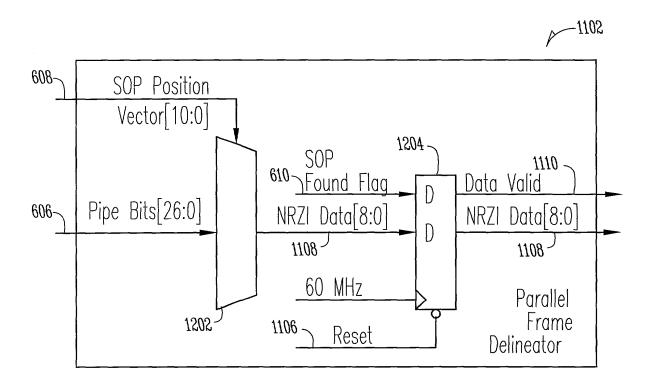


Fig. 12

TITLE: PIPELINED, UNIVERSAL SERIAL BUS PARALLEL FRAME DELINEATOR AND NRZI DECODER

INVENTORS NAME: Dean Warren et al. DOCKET NO.: 884.311US1

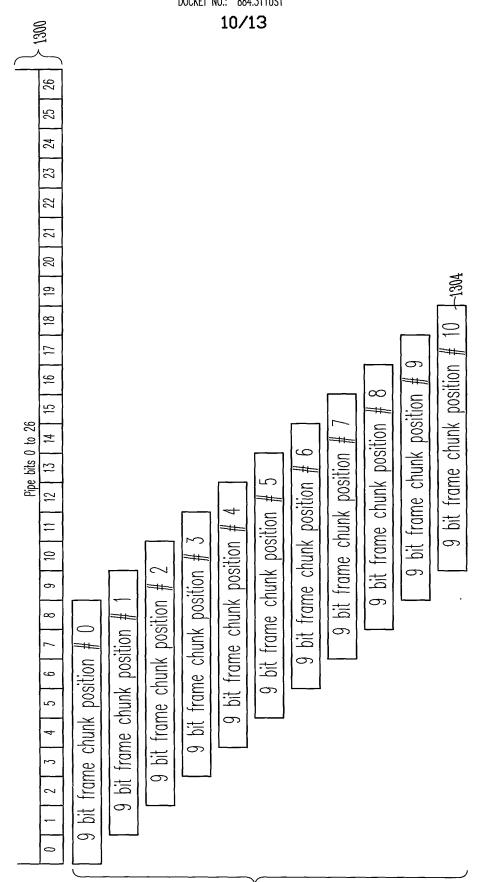


Fig. 13

28

2

24

23

 $^{2}$ 

21

2

 $\approx$ 

9

 $\sim$ 

 $\leq$ 

 $\infty$ 

9

bits 0 to 26

TITLE: PIPELINED, UNIVERSAL SERIAL BUS PARALLEL FRAME DELINEATOR AND NRZI DECODER INVENTORS NAME: Dean Warren et al. DOCKET NO.: 884.311US1 11/13 comparator  $\infty$ SOP comparator bit 50P comparator SOP SOP comparator SOP comparator <u>:</u> p:t  $\infty$ SOP comparator  $\infty$ SOP comparator SOP comparator  $\infty$ SOP comparator 0  $\infty$ j; comparator  $\infty$ Ħ SOP comparator 0  $\infty$ Ē: SOP  $\infty$ p:  $\infty$ D:  $\infty$ Đ:  $\infty$ 0 0 0 frame 

INVENTORS NAME: Dean Warren et al.

DOCKET NO.: 884.311US1

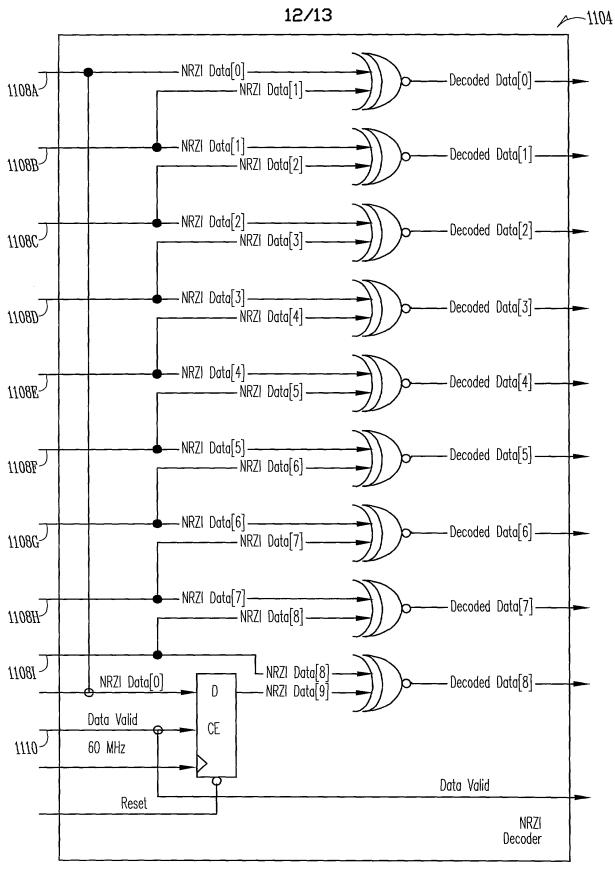


Fig. 15

INVENTORS NAME: Dean Warren et al. DOCKET NO.: 884.311US1

